-	785	713/193.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:19
-	428	327/545.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:19
-	931	711/163.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:27
-	9961	(FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 18:05
-	173386	(key (mask)) and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 18:05
-	664	((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:38
-	6640475	(first one primary 1st) and (second$3 two 2nd alternative)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 18:06
-	4	((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second$3 two 2nd alternative))) and 711/163.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:45
-	138	((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second$3 two 2nd alternative))) and "111"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:45
-	0	((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second$3 two 2nd alternative))) and 327/545.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:45

-	6	(((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second$3 two 2nd alternative))) and 713/193.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:48
-	1074	713/2.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:49
-	787	713/100.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:49
-	0	713/1190.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:49
-	160	713/190.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:49
-	785	713/193.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:49
-	239	713/194.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:49
-	171	713/187.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:49
-	3	(((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second$3 two 2nd alternative))) and 713/2.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:50
-	2	(((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second$3 two 2nd alternative))) and 713/100.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:52

-	0	((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second$3 two 2nd alternative))) and 713/1190.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:52
-	1	((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second$3 two 2nd alternative))) and 713/187.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:52
-	6	((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second$3 two 2nd alternative))) and 713/190.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:55
-	5	((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second$3 two 2nd alternative))) and 713/194.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:53
-	6	((((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second$3 two 2nd alternative))) and 713/193.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:55
-	484	(((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random)) and ((first one primary 1st) and (second$3 two 2nd alternative))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 15:56
-	7	(FPGA with chip) and (memory storage "hard drive") and ("Field Programable Interconnect Components" FPICs)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 16:06
-	4	((FPGA with chip) and (memory storage "hard drive") and ("Field Programable Interconnect Components" FPICs)) and (($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 16:05

-	127	(FPGA with chip) and (memory storage "hard drive") and (((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and (pseudo random))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 16:43
-	2	09/780681	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 16:39
-	1	(FPGA with chip) and (contact adj layer) and (polysilicon adj wire)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 18:07
-	179	(polysilicon adj wire)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 16:43
-	1	(polysilicon adj wire) and (FPGA)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 16:45
-	489	(polysilicon) and (FPGA)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 16:45
-	1165	(SRAM "Static Random Access Memory") with FPGA	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 16:57
-	79	((FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays") and ((integrated adj circuits) IC)) and ((key (mask)) and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)) and (CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature) and ((SRAM "Static Random Access Memory") with FPGA)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 16:57
-	965	"53" and CBC	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 17:19
-	2	("6356637").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 17:36
-	8	(("6324286") or ("4878246") or ("6615349") or ("5036468")).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 17:38
-	2	("6357037").PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 18:05

-	2	(FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays" (integrated adj circuits) IC key mask $2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3 CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature first one primary 1st second$3 two 2nd alternative) and (("6357037").PN.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 18:07
-	2	(FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays" (integrated adj circuits) IC key mask $2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3 CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature first one primary 1st second$3 two 2nd alternative FPGA chip contact layer polysilicon adj wire) and (("6357037").PN.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 04:49
-	1262	burnham-$-$.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 18:12
-	18	burnham-$-$.in. and FPGA	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 18:12
-	18	(FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays" (integrated adj circuits) IC key mask $2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3 CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature first one primary 1st second$3 two 2nd alternative FPGA chip contact layer polysilicon adj wire) and (burnham-$-$.in. and FPGA)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 23:33
-	0	(FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays" (integrated adj circuits) IC key mask $2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3 CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature first one primary 1st second$3 two 2nd alternative FPGA chip contact layer polysilicon adj wire) and (burnham-$-$.in. and FPGA) and mask	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 18:29
-	78	713/100.ccls. and mask	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 18:29
-	19	((US-6122716-$ or US-5666516-$ or US-6324676-$ or US-6640305-$ or US-6633905-$ or US-6408432-$ or US-6324288-$ or US-6279045-$ or US-6215327-$ or US-6151677-$ or US-6147890-$ or US-6134707-$ or US-5717394-$ or US-5687325-$ or US-6772387-$ or US-6560743-$ or US-6357037-$ or US-6118869-$ or US-6049222-$).did. or (US-20040111631-$ or US-20020099948-$ or US-20020026539-$ or US-20010032318-$ or US-20010015919-$ or US-20010037482-$).did.) and mask	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/08 18:29

-	0	713.200.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 00:20
-	0	380.200.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 00:20
-	0	380.30.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 00:21
-	53	714/100.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 00:22
-	371	717/124.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 00:23
-	180	717/139.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 00:22
-	6	717/139.ccls. and FPGA	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 00:22
-	10	Kelem-$-$.in. and FPGA	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 01:02
-	48	(xilinx same FPGA) and mask and (distribut$3 provider) and multiple and bitstream and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 01:16
-	56	(xilinx same FPGA) and mask and multiple and bitstream and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 01:18
-	8	((xilinx same FPGA) and mask and multiple and bitstream and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)) not ((xilinx same FPGA) and mask and (distribut$3 provider) and multiple and bitstream and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 01:16
-	56	(xilinx same FPGA) and mask and bitstream and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 01:18
-	132	(xilinx same FPGA) and mask and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 01:18
-	76	((xilinx same FPGA) and mask and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3)) not ((xilinx same FPGA) and mask and bitstream and ($2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/09 01:19

| - | | 2 | (FPGA "Field Programable gate array" FPGAs "Field Programable gate arrays" (integrated adj circuits) IC key mask $2crypt$3 $2cipher$3 $2compress$3 $2scrambl$3 CRC "cyclic Redundancy Check" checksum MAC "message authentication code" signature first one primary 1st second$3 two 2nd alternative FPGA chip contact layer polysilicon adj wire) and (("6061449").PN.) | USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB | 2004/08/09 03:50 |